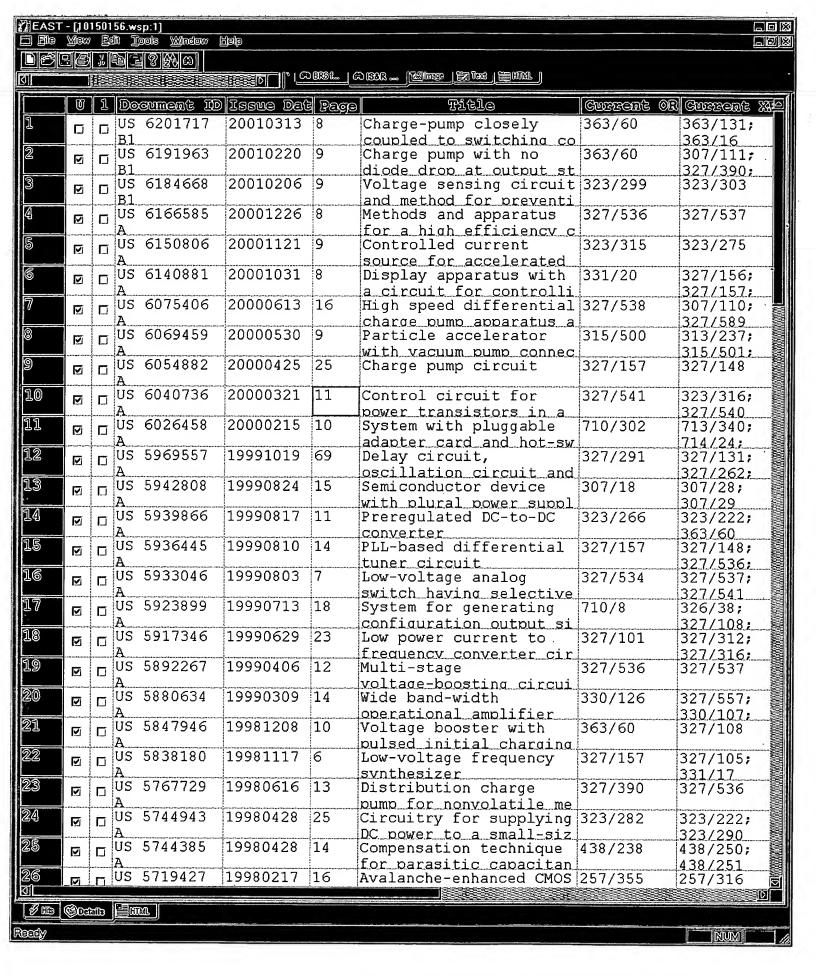
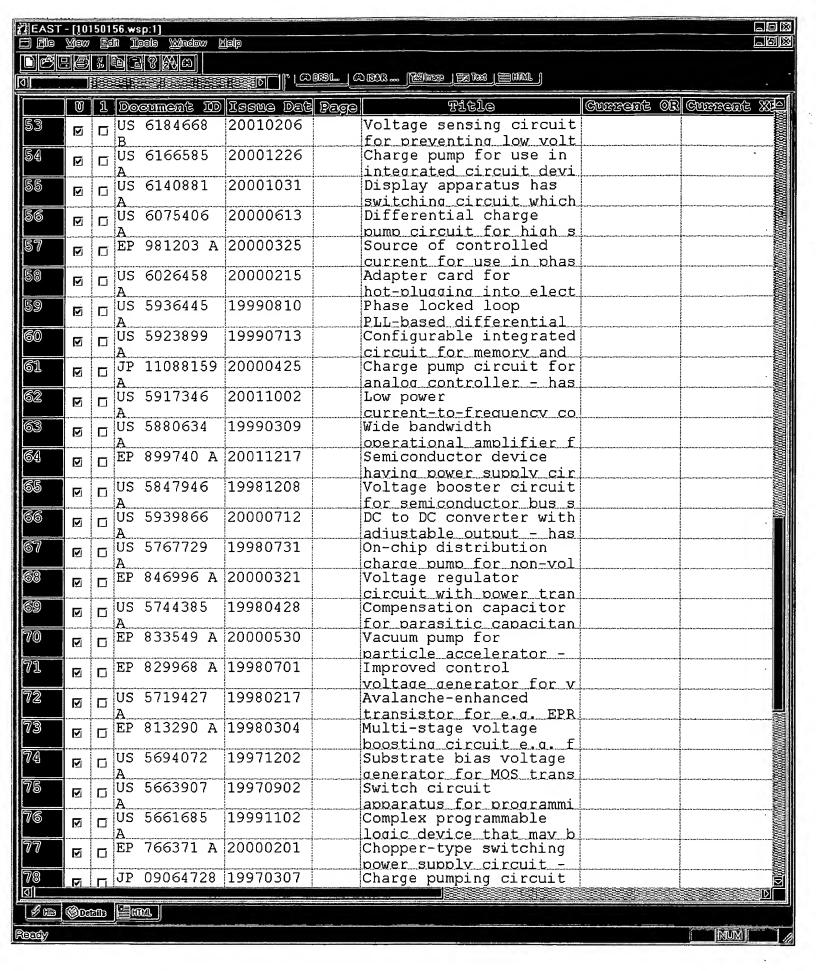
	Туре	L #	Hits	Search Text	DBs	Time Stamp	Comment
14	BRS	L14		clock adj generating same delay	USPA T; US-P GPUB; JPO; DERW ENT	2002/10/1 7 10:32	
15	BRS	L15		14 and ((DLL delay adj locked adj loop) (PLL phase adj locked adj loop))	USPA T; US-P GPUB ; JPO; DERW ENT	2002/10/1 7 10:34	



					delp	ama sa waxaadahi		a	
BRS f A ISAR   Sall mage   FA Text   HTML									
(Marin	U	1	Do	cument ID	Issue Dat	Page	Title	Current OR	Current XE
27	Image: section of the			5694072	19971202		Programmable substrate	327/537	327/536;
	.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		A_	5660007	10070000		bias generator with cur	265 (105 10	327/540 365/185:27
28	v		US	5663907	19970902		Switch driver circuit	;	365/185:2
.29			A HS	5663686	19970902	i 	for providing small sec Charge pump circuit and	331/8	327/157;
23	v	П	D.S	3003000	15570502		phase locked loop circu	331,0	331/17;
30	M		US	5661685	19970826		Programmable logic	365/185.22	365/185.01
	ΙΝ	L	A			: : :	device with configurabl		, <u> </u>
31	V	П	US	5642313	19970624		, -	365/185.25	
		ļ	Α		10000000		for a memory device	227/261	365/204:
32	v	П	US	5627488	19970506		Delay circuit,	327/261	327/132; 327/262;
33		ļ	A	5623222	19970422	[ 	oscillation circuit and Voltage converting	327/259	327/295;
33	V	П	D)	3023222	13310422		circuit and multiphase	321,203	327/47
34	M	П	ŪS	5604694	19970218	•	Charge pump addressing	365/96	327/536;
	151	L	Α					, } } }	365/225.7
35	Ø	П	US	5532916	19960702		Voltage converting	363/62	307/110
In 6			A_		1006000		circuit and multiphase	255 /25	2021526
36	v		US	5495436	19960227		Anti-fuse ROM	365/96	327/536;
37			A	5465061	19951107		programming circuit Low-consumption	327/112	365/225.7 327/108;
) (	Ø	П	D.S	3403001	19901107		low-consumption low-noise charge-pump c	327/112	327/111;
38	_	П	US	5461557	19951024		Voltage converting	363/60	307/110
	Ø	<b>I</b> l	A				circuit and multiphase		
39	V	П	US	5412603	19950502		Method and circuitry	365/189.01	
			Α	5.40.40.50	10050404		for programming floatin	007/100	365/185.1 <b>U</b>
40	Ø		US	5404053	19950404			327/108	327/379;
41			A	5392205	19950221		the maximum current in Regulated charge pump	363/59	327/390; 323/285;
7 4	Ø	П	A	0332200	15500221		and method therefor	303703	323/2037
42	v	П	US	5381056	19950110		CMOS buffer having	326/21	326/121;
	121		A				output terminal overvol	***************************************	326/68;
43	Ø		US	5373257	19941213		Phase synchronization	331/8	331/17;
4.4			A	5359298	19941025		circuit having a loop f	331/2	331/25
44	N.		N Z	5359298	19941025		vco naving voltage-to-current conv		331/108B; 331/14;
45		_	US	5302919	19940412		VOILade-to-current conv VCO having	331/2	331/108B;
	Ø	П	A_				voltage-to-current conv		331/14;
46	v	П	US	5234319	19930810		Sump pump drive system	417/40	417/45
	.,,		Α		20000530			~~~~~~	
47	Ø	П	US	5212456	19930518		Wide-dynamic-range	330/261	327/534;
48			HS.	5180929	19930119		amplifier with a charge Voltage measuring	327/50	330/253; 327/103;
30	Ø		D.	3100929	19930119		circuit	327730	327/51;
49	<b>v</b>	П	US	4847519	19890711	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Integrated, high speed,	327/536	327/574;
	IAI		Α	***************************************		.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	zero hold current and d		330/257
50	Ø	П	US	4354124	19821012		Digital phase	327/12	327/551;
F 7			A	1001006	00000510		comparator circuit		375/328
51	Ø		EP	1081836	20020510		Charge pump circuit has		
52			A US	6191963	20010220		NMOS transistors Integrated circuit with	***************************************	
52 •	<b>▽</b>			3131300	,				r M
	<b>⊘</b> Def	tails	В	rmil					
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Ready									NUM /

**M**EAST - [10150156.wsp:1]



EAST	- [10 ⊻iew	1501 <u>E</u> c	<b>56.w</b> dit ☐		<u>-l</u> elp					_ I
【● BRS f										
	บ	1	Do	cument ID	Issue Dat	Page	Title	Current OF	Current	ΧĒ
74	v		US	5694072	19971202		Substrate bias voltage			
75			A	5663907	19970902	! ! ! ! !	generator for MOS trans Switch circuit	***************************************		
13			7	3003907	19970902	* * * * * * * * * * * * * * * * * * *	apparatus for programmi		***************************************	
76	Ø	П	US	5661685	19991102	• • • • • • • • • • • • • • • • • • •	Complex programmable			
	121	[ ]	Α_	***************************************		! ! !	logic device that may b	***************************************	·	
77	Ø		EP	766371 A	20000201		Chopper-type switching		and the second	
78		ļ	JΡ	09064728	19970307		power supply circuit - Charge pumping circuit			
	M		A				in PLL - in which up an			
79	Ø	П	FR	2738424	19990803		MOS transistor low			
80	<b>-</b>		A	5604694	19970218		voltage analog switch -			
80			7	3604694	19970218		Charge pump circuit for effectively providing p			
81	v		JP	08190798	20010611		Non-volatile	***************************************		
	1		Α_	nantalan hama arang an			semiconductor memory e.	**************************************		
82	v		EP	713165 A	19960522		Voltage boost circuit			
83			JР	08070242	19990715		for flash EPROM memory Delay circuit used for	\$1.45\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		
	Ø	П	A				PMOS IC - has charging			
84	V		JP	08065111	19990715		Oscillation circuit for			
8.5		ļ	A	5495436	19960227	***************************************	semiconductor integrate		<u> </u>	
0.5	Ø		A	3493430	19900221		On-chip programming circuit for anti-fuse e			
86	Ø		EΡ	681296 B	20011025	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Programming method for		•	
0.7				611000 5	10000100		floating gate memory ce	······································		
87	Ø		EP	614282 A	19980102		Charge pump circuit for frequency synthesiser -			
88	Ø	П	ΕP	595632 A	19980212	**********************	Phase synchronisation	***************************************		
	157		ļ				circuit for magnetic di			
89	Ø		EP	587931 A	20000115		Complementary MOSFET			
90	Ø		EP	585925 A	19980813		buffer circuit - uses t Charge-pump step-up	***************************************		
	N	I					type voltage converting			
91	V		ΕP	574646 A	19980416		Circuit controlling			
92			ΠC	5234319	19930810		maximum current in MOS Building sump pump			
			A	0204010	19990010		drive system with normal			
93	Ø		US	5212456	19930518		Linear voltage	~*************************************		
94	<b> </b>		A	540948 A	10020510		amplifier circuit - has			
94	V		СP	340946 A	19930312		Regulated charge pump for voltage generator f			
95	v		US	5180929	20010307		Voltage measuring			
0.6			A	F000010	10061100		circuit for unknown vol			
96	Ø		US 7	5302919	19961120		Voltage-controlled oscillating circuit for			
97	v		US	4847519	19890711		Integrated zero hold	***************************************	[ 	
		L/	A_	/m/			current and delay compe	***************************************	•	
98	区		DE	3022746	19810108		Injection logic digital			.
	11		Α		[ <u></u>		phase comparator - simu		<u></u>	
(I										
Ready									NUM	1